

Fig. 1A

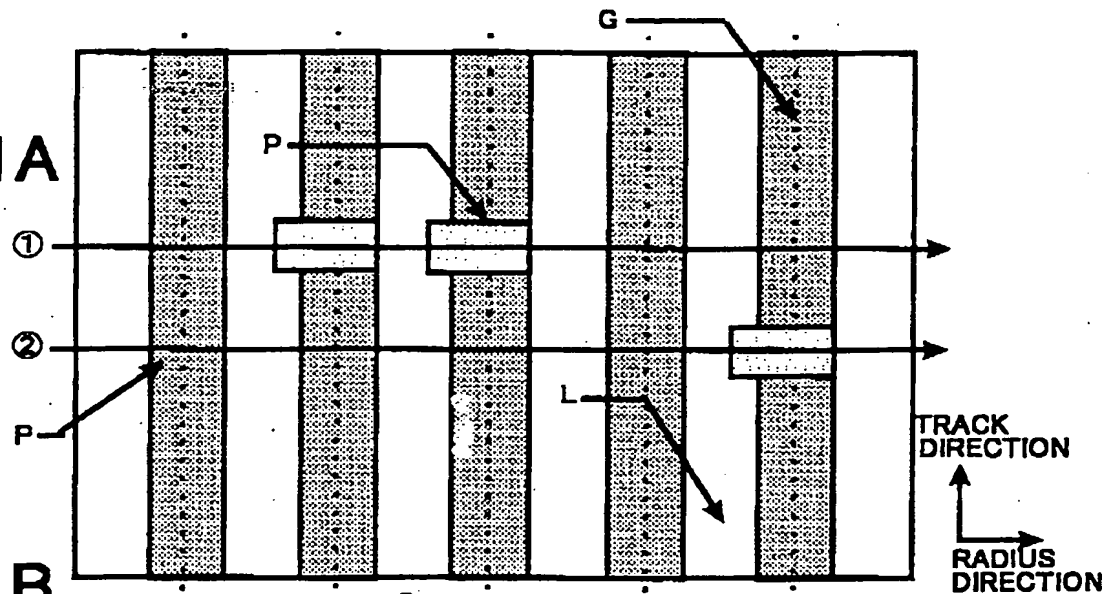


Fig. 1B

CROSS SECTION
①

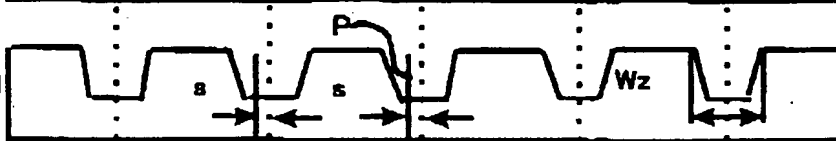
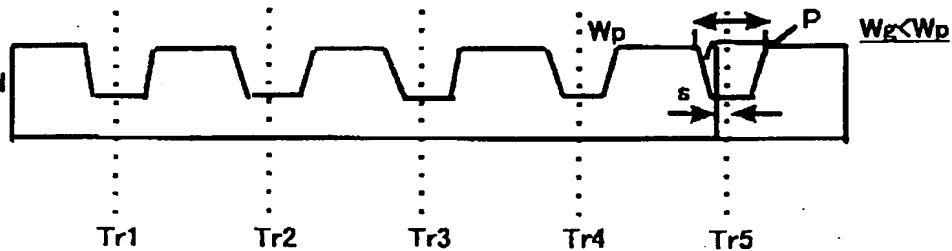


Fig. 1C

CROSS SECTION
②



Tr1

Tr2

Tr3

Tr4

Tr5

Fig. 2A

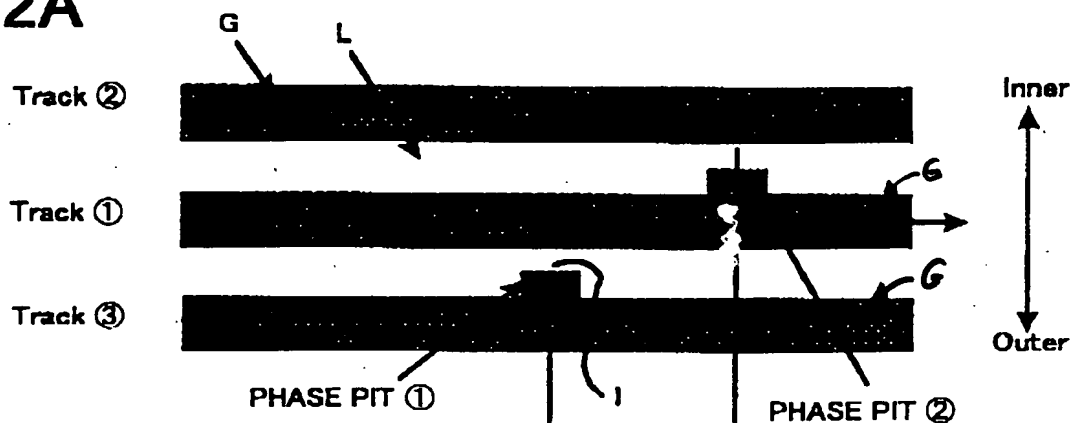


Fig. 2B

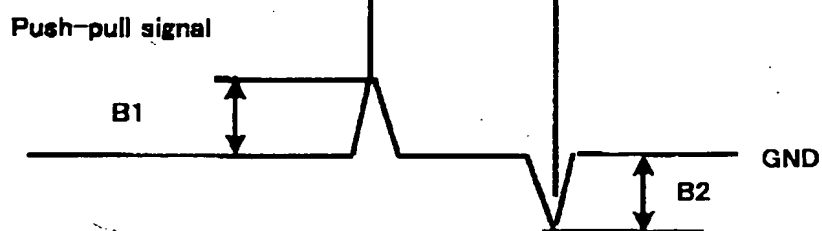
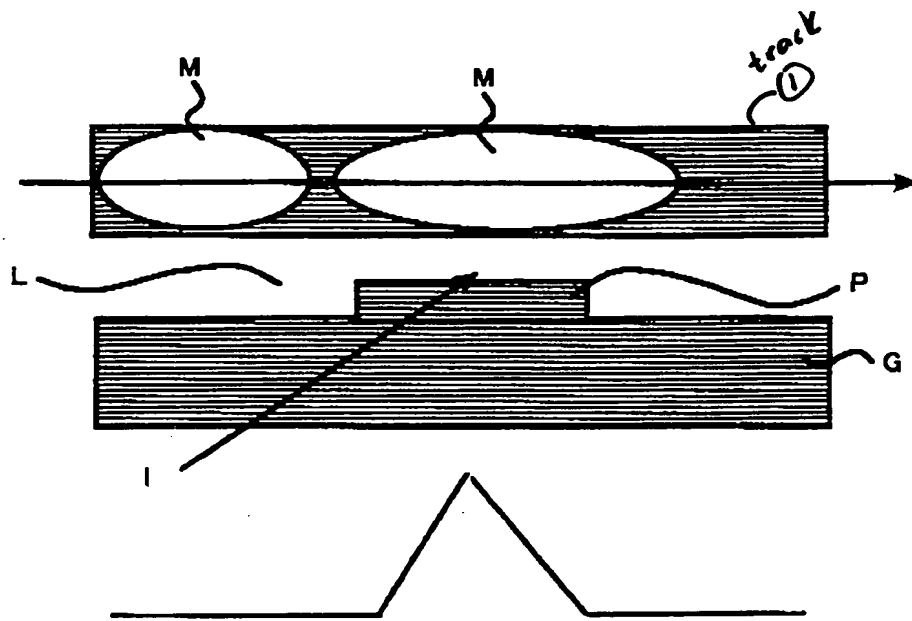


Fig. 3



00405570.092499

Fig. 4

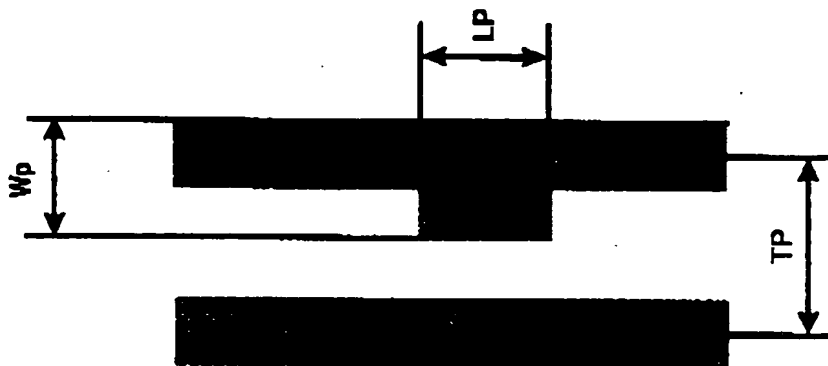
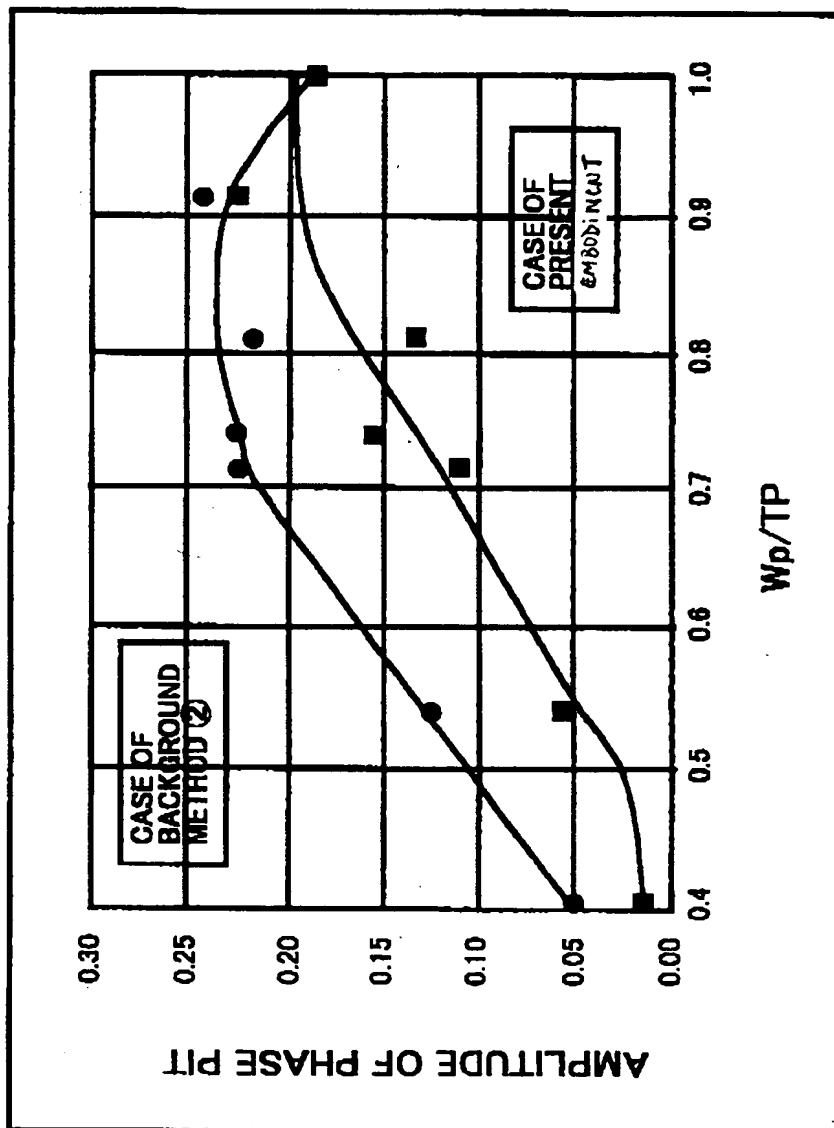
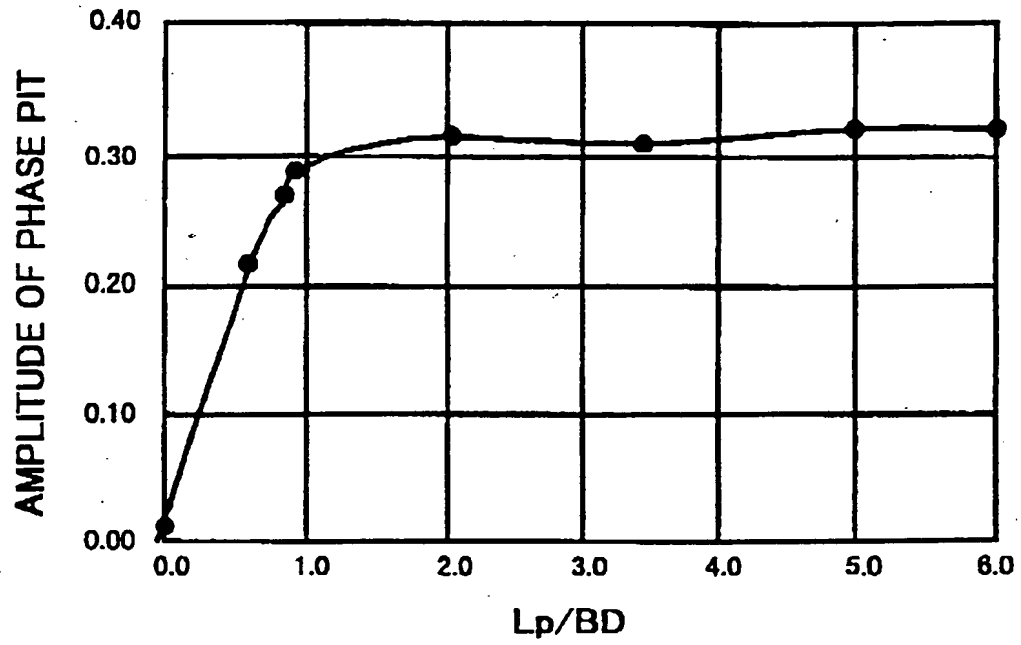


Fig. 5



0405570.029060

Fig. 6A

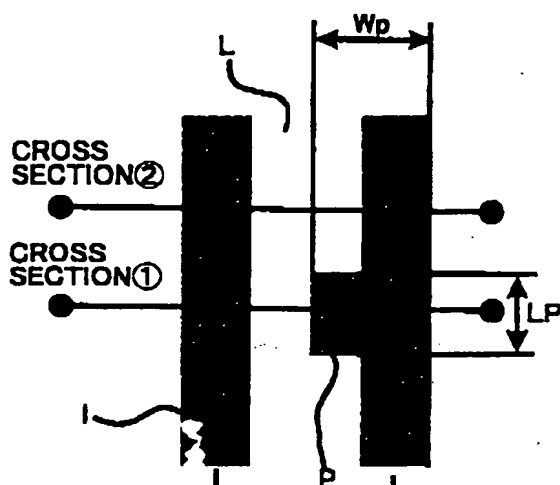


Fig. 6B



Fig. 6C

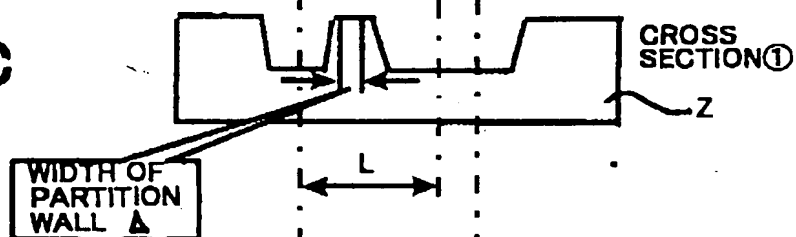


Fig. 6D

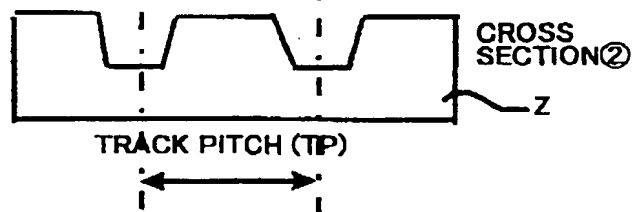
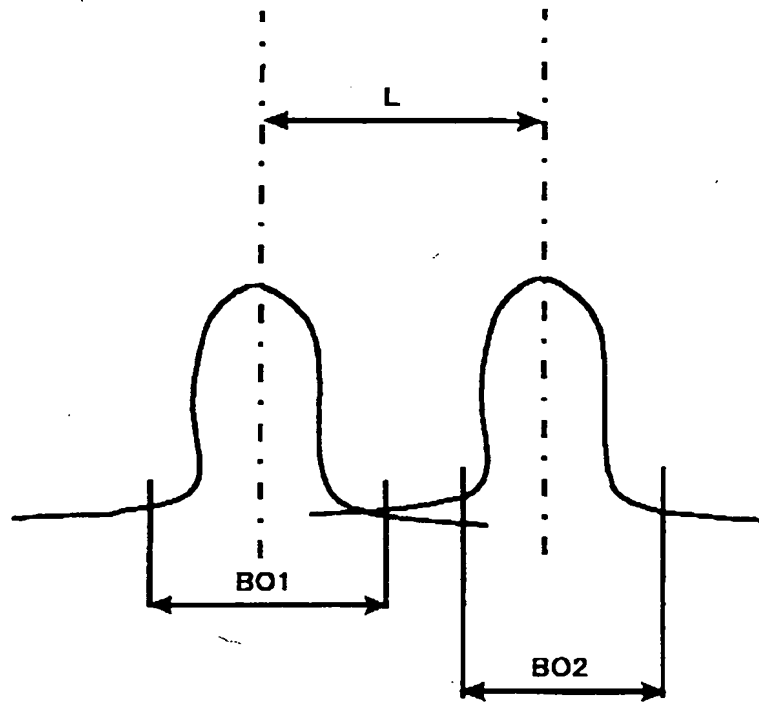


Fig. 7



00405370.032499

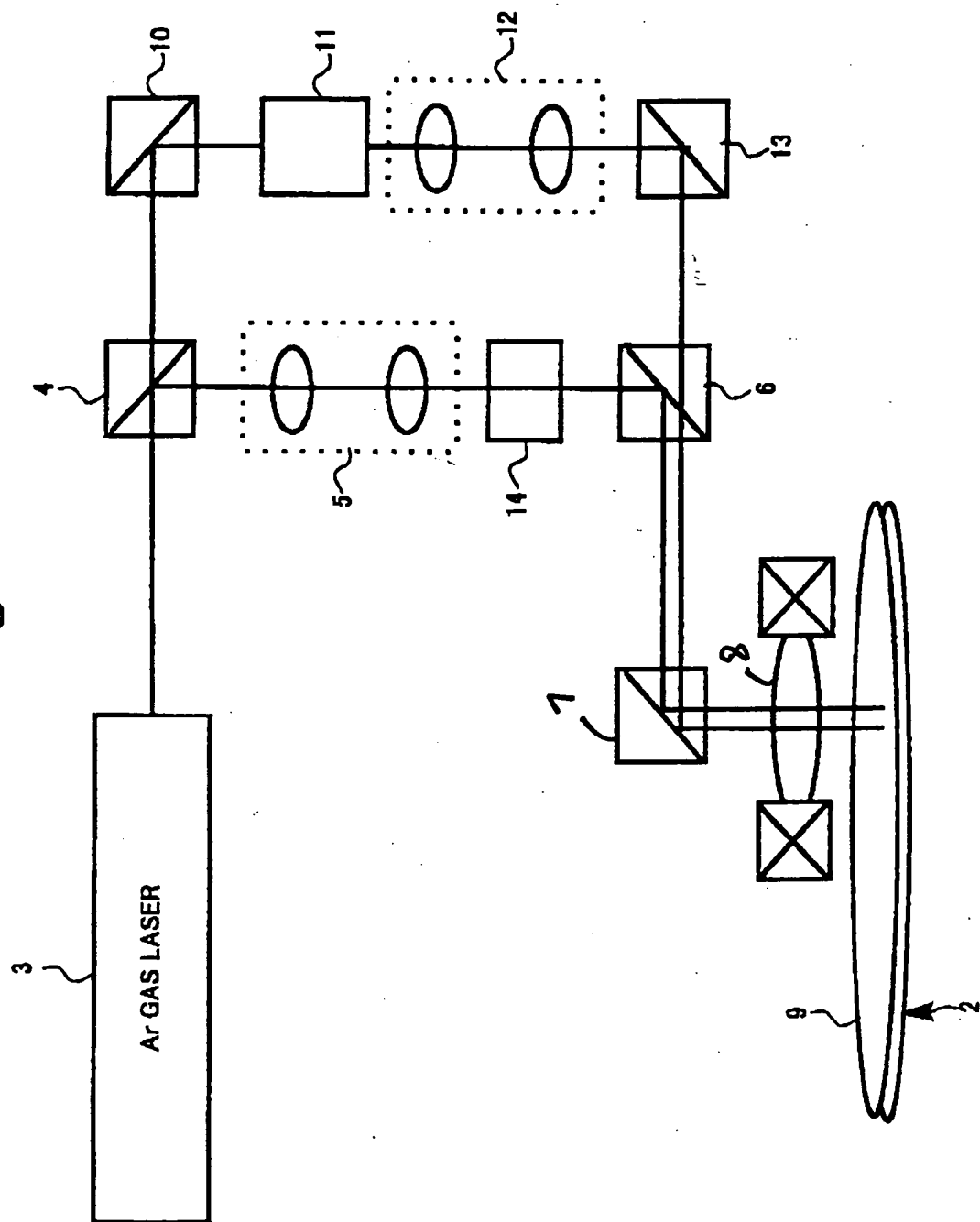


Fig. 9A

MAKING OF RESIST
ORIGINAL BOARD

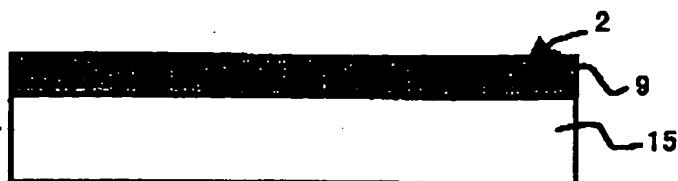


Fig. 9B

EXPOSING OF
ORIGINAL BOARD

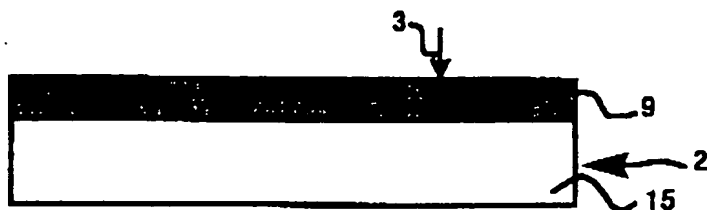


Fig. 9C

DEVELOPING

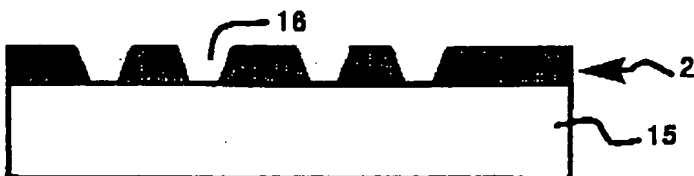


Fig. 9D

PROCESSING OF
CONDUCTIVE FILM

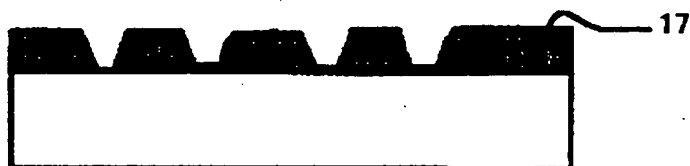


Fig. 9E

NI ELECTROFORMING

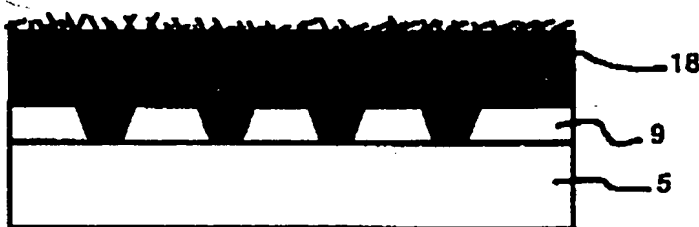


Fig. 9F

PEELING-OFF · CLEANING ·
POLISHING OF REAR SURFACE ·
FINAL PROCESSING



Fig. 10

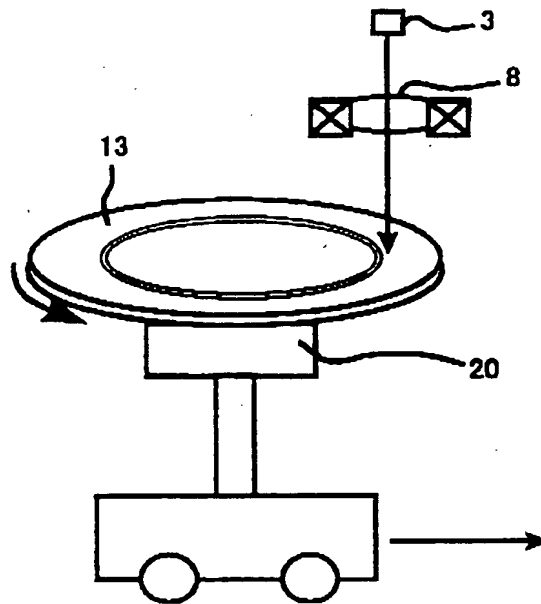


Fig. 11A

PRIOR ART

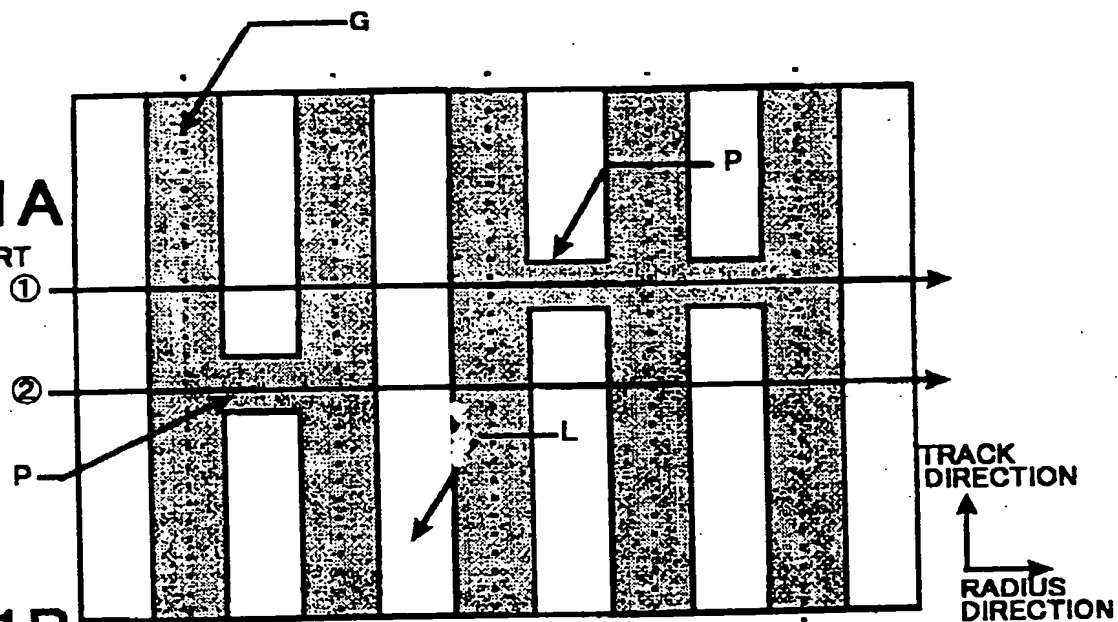


Fig. 11B

PRIOR ART

CROSS SECTION ①

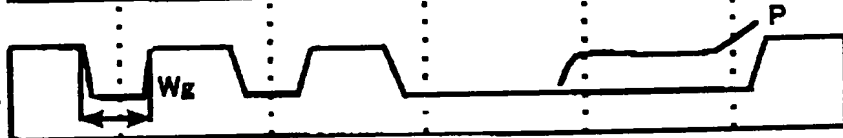
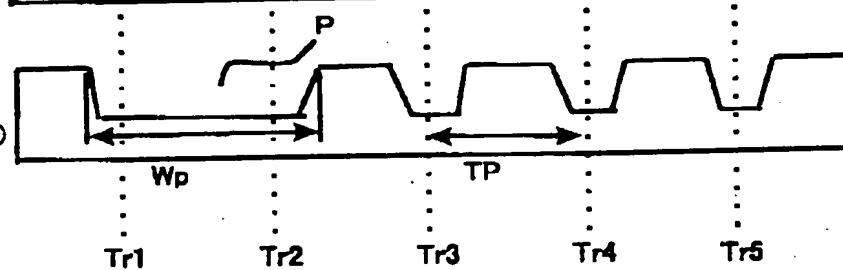


Fig. 11C

PRIOR ART

CROSS SECTION ②



PRIOR ART

Fig. 12A

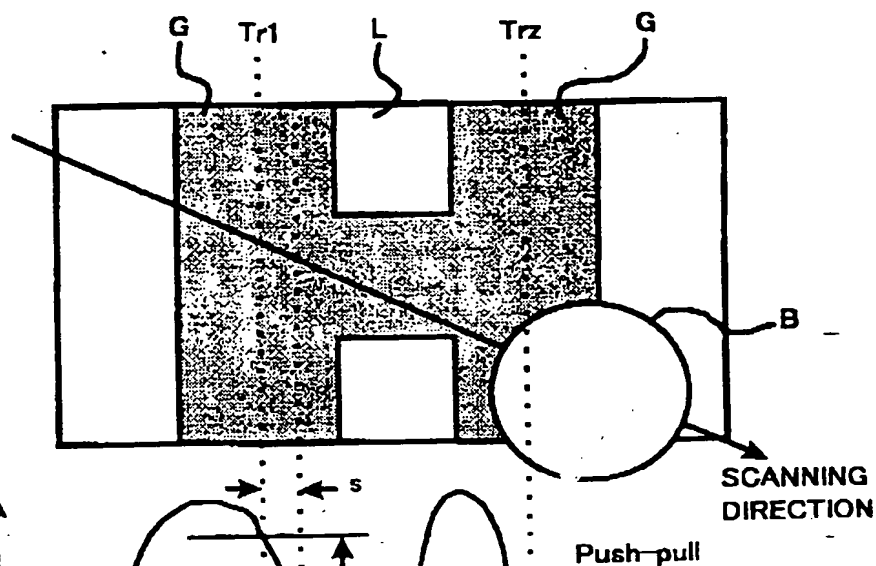
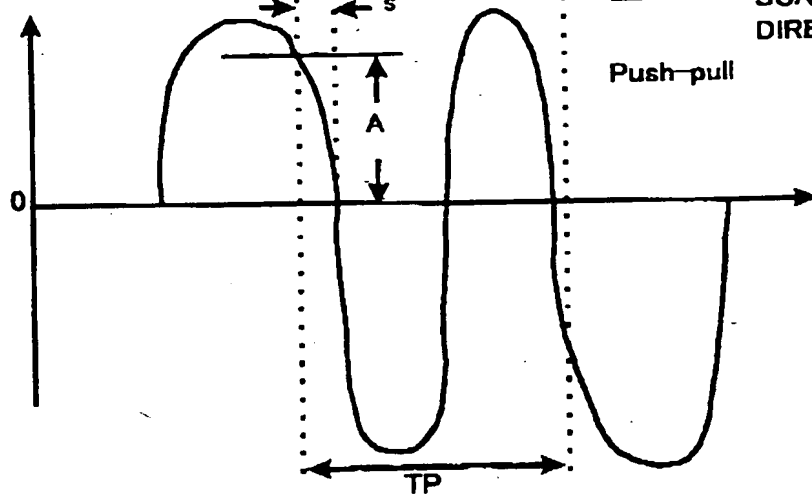


Fig. 12B



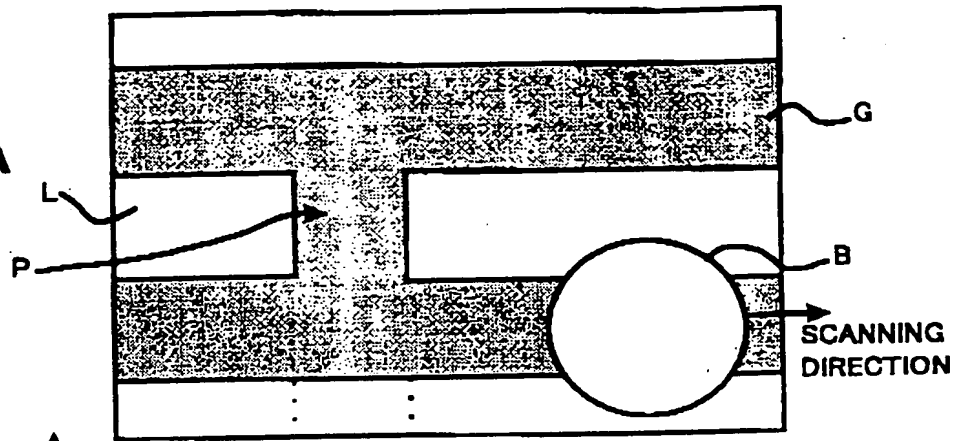
[illegible]

Fig. 13B

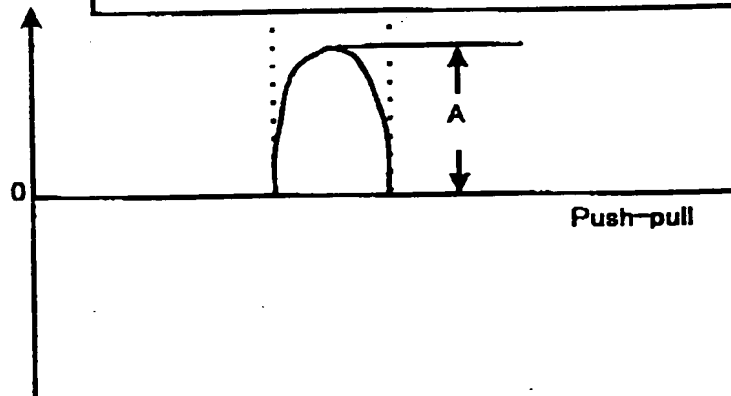


Fig. 14A

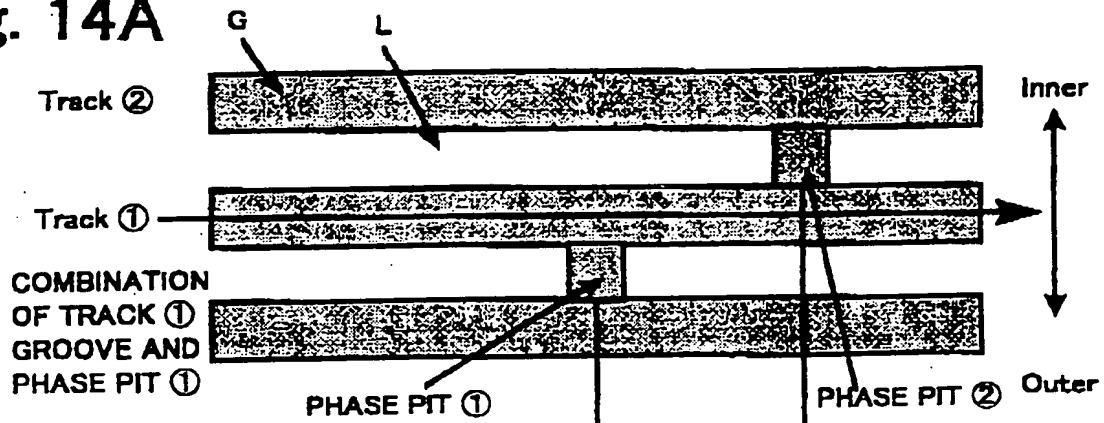
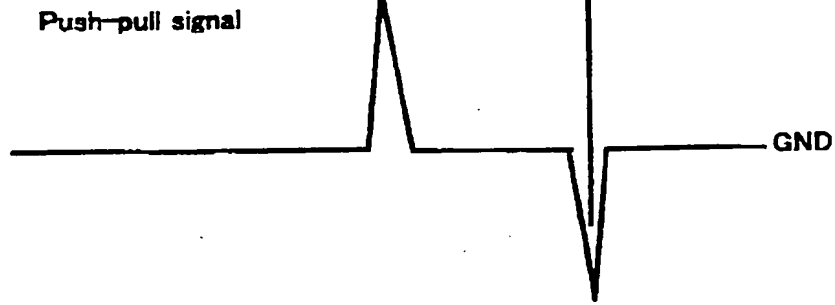


Fig. 14B



604260 02590460

Fig. 15A

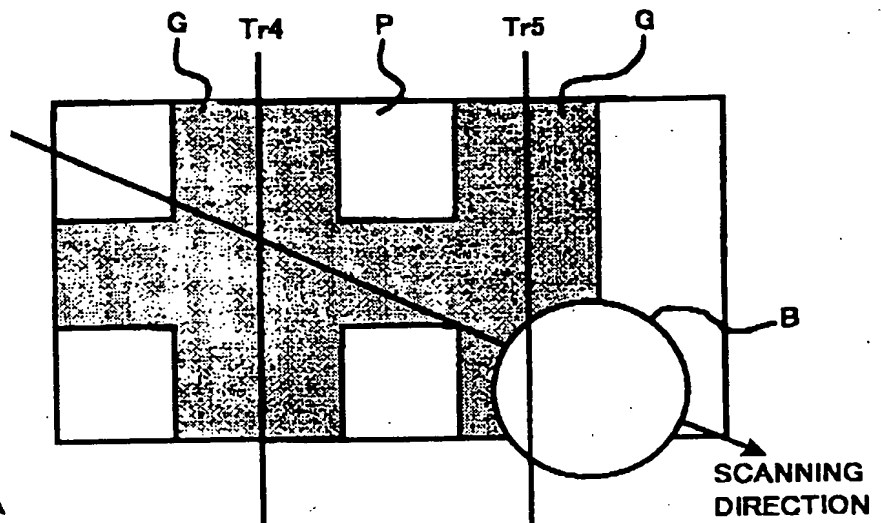
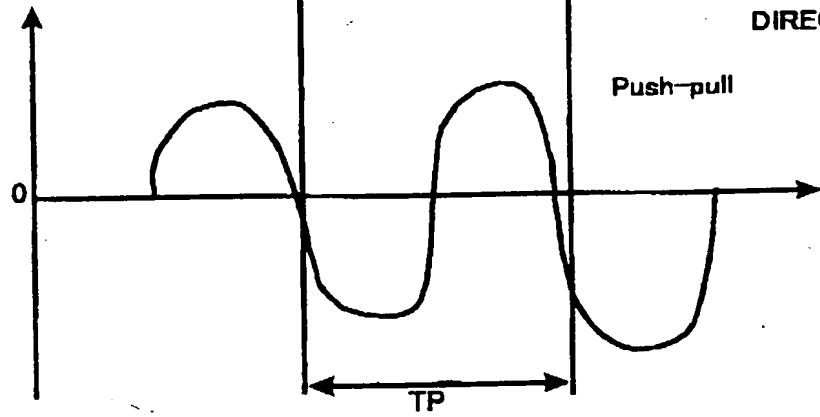


Fig. 15B



0045570 0045570 0045570

Fig. 16A

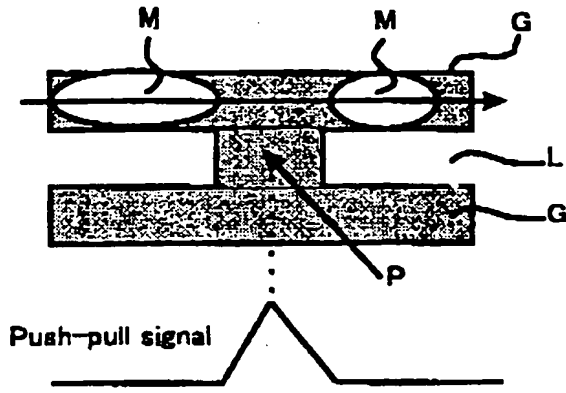


Fig. 16B

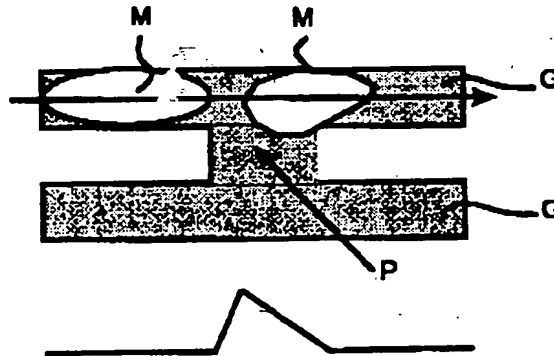


Fig. 16C

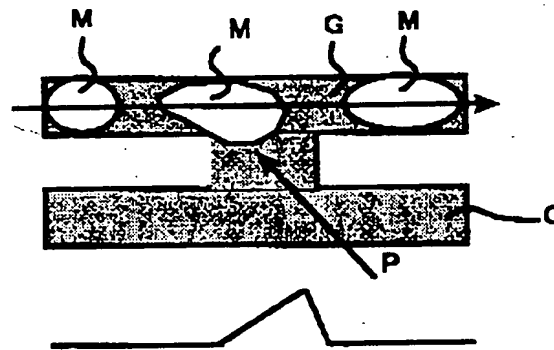


Fig. 16D

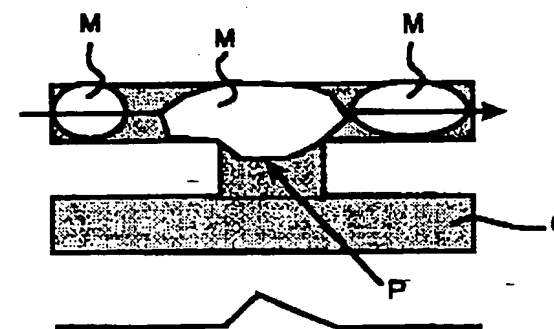


Fig. 17A

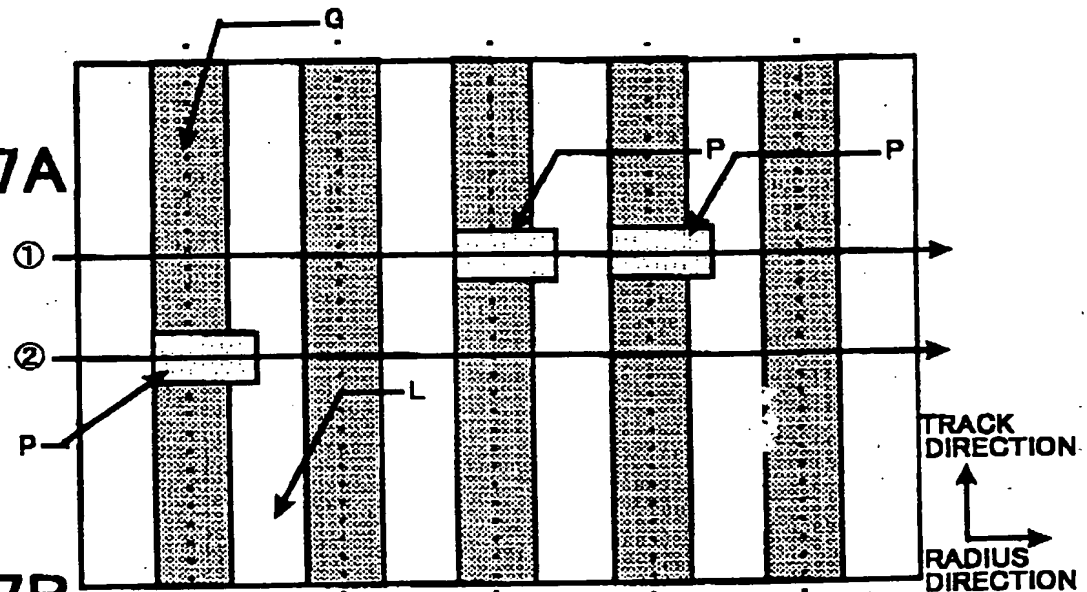


Fig. 17B

CROSS SECTION
①

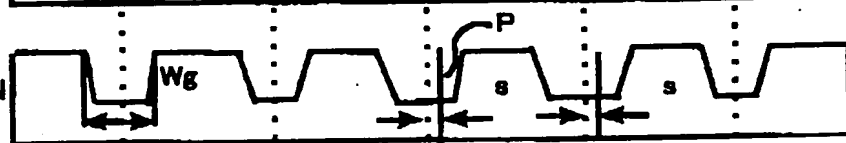
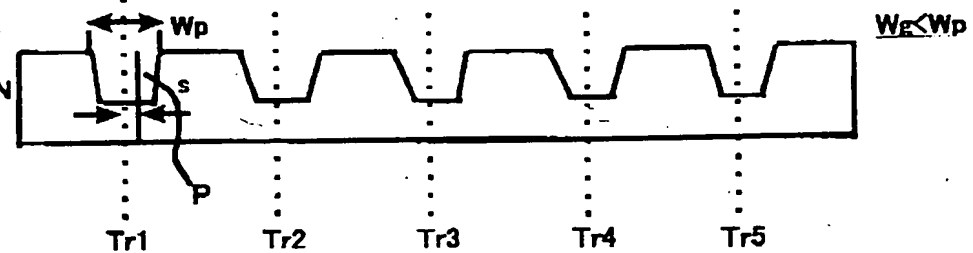


Fig. 17C

CROSS SECTION
②



Private Art

Fig. 18A

PRIOR ART
Track ②

COMBINATION
OF TRACK ①
GROOVE AND
PHASE PIT ①

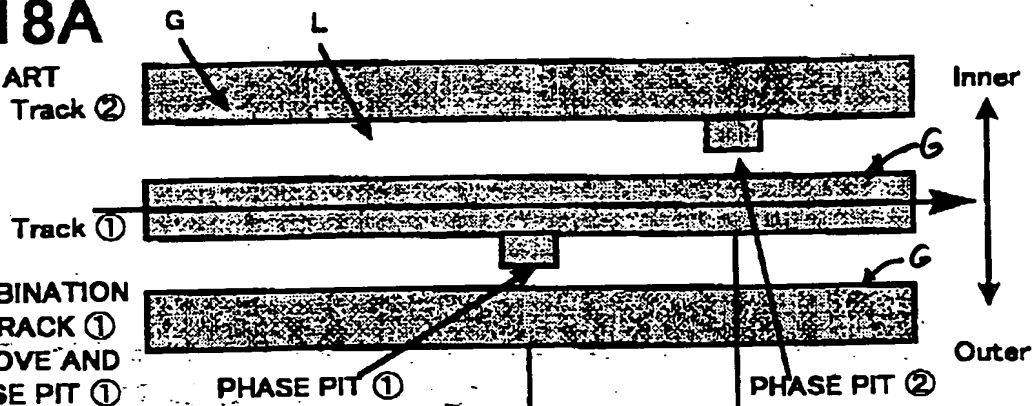
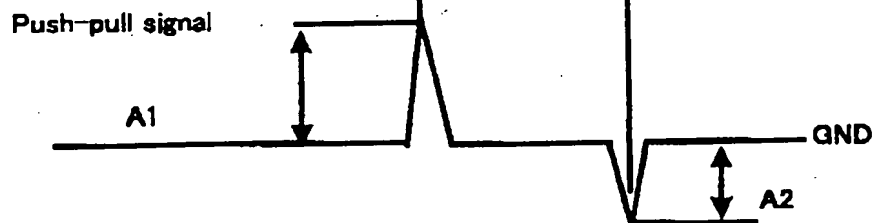


Fig. 18B

PRIOR ART



fur